

## PART B - FEE(S) TRANSMITTAL

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I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Mail Stop ISSUE FEE address above, or being facsimile transmitted to the USPTO (571) 273-2885, on the date indicated below.

Joseph Jong

(Depositor's name)

October 10, 2006

(Signature)

(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/810,489	03/26/2004	Kazimierz Szczepinski	INFN/0073	8122

TITLE OF INVENTION: INTEGRATED CIRCUIT WITH A TEST CIRCUIT

10/11/2006 RNEBRAH1 00000014 200782 10810489

01 FC:1501 1400.00 DA  
 02 FC:150A 300.00 DA

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$0	\$1700	10/10/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
PHAM, LONG	2814	257-213000

Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.

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Patterson &amp; Sheridan, L.L.P.

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PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

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Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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Change in Entity Status (from status indicated above)

☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.

☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Date October 10, 2006

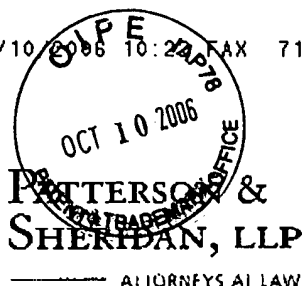
Typed or printed name

Joseph M. Jong

Registration No. 42,698

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**DATE:** October 10, 2006  
**FILE NO:** INFNWB0073 (INF 2284-US)  
**TO:** **Mail Stop ISSUE FEE**  
Commissioner for Patents, USPTO  
**FAX NO:** 1-571-273-2885  
**FROM:** Gero G. McClellan / Joseph M. Jong  
**PAGE(S) with cover:** 3  
**ORIGINAL TO FOLLOW?** ☐ YES ☒ NO

**RE:**

**TITLE:** INTEGRATED CIRCUIT WITH A TEST CIRCUIT  
**U.S. SERIAL NO.:** 10/810,489  
**FILING DATE:** March 26, 2004  
**INVENTOR:** Szczpinski et al.  
**EXAMINER:** Long Pham  
**ART UNIT:** 2814  
**CONF. NO.:** 8122

**Attached are:**

1. Part B - Fee(s) Transmittal for Application No. 10/810,489
2. Copy for charge to Deposit Account No. 20-0782 / INFNWB0073 / GGM

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